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174/297

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

BIT ERROR RATE TESTER IMPLEMENTED
IN A PROGRAMMABLE LOGIC DEVICE

the specification of which

☒ is attached hereto

☐ was filed on _____ as
Application No. _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I do not know and do not believe that the invention was ever patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application.

I do not know and do not believe that the invention was in public use or on sale in the United States of America more than one year prior to this application.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known by me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
			[]	[]
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

(Provisional Appln. No.)

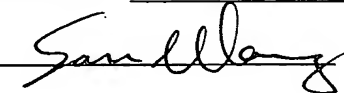
(Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known by me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

_____	_____	_____
(Application No.)	(Filing Date)	(Status) (patented, pending, abandoned)
_____	_____	_____
(Application No.)	(Filing Date)	(Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first inventor San Wong

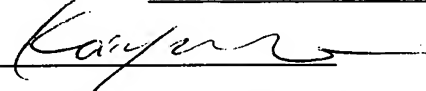
Inventor's signature  11/20/03
Date

Residence 11671 Southshore Court, Cupertino, CA 95014

Citizenship China

Post Office Address Same

Full name of second inventor Kaiyu Ren

Inventor's signature  11/20/03
Date

Residence 1488 Stubbins Way, San Jose, CA 95132

Citizenship China

Post Office Address Same

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : San Wong and Kaiyu Ren

Application No. : Not yet assigned Confirmation No. : Not yet assigned

Filed : Concurrently Herewith

For : BIT ERROR RATE TESTER IMPLEMENTED IN A PROGRAMMABLE LOGIC DEVICE

Group Art Unit : Not yet assigned

Assignee : Altera Corporation

POWER OF ATTORNEY BY ASSIGNEE

Sir:

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record, by virtue of an assignment submitted concurrently herewith for recording, of the above-identified patent application, hereby elects to conduct the prosecution of the above-identified patent application to the exclusion of the inventors. The assignee hereby revokes any powers of attorney previously granted and appoints Derek E. Minihane, Reg. No. 39,774, Robert R. Jackson, Reg. No. 26,183, Jeffrey H. Ingerman, Reg. No. 31,069, and Alexander Shvarts, Reg. No. 47,943, its attorneys and agent, respectively, with power of substitution, and with power of appointment of associate attorneys, and of revocation of their powers, to prosecute this application and any divisions, continuations in whole or in part, renewals and

reissues of the same, and to transact all business in the
Patent and Trademark Office connected therewith;

and assignee requests that communications be sent
to:

Alexander Shvarts
Fish & Neave
1251 Avenue of the Americas
New York, New York 10020-1105

and that telephone calls be directed to:

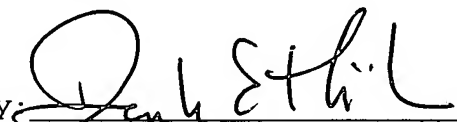
Alexander Shvarts
(212) 596-9000.

The undersigned hereby certifies that the
evidentiary documents have been reviewed and, to the best of
the undersigned's knowledge and belief, title is in assignee.

The undersigned hereby declares that all statements
made herein of my own knowledge are true and that all
statements made on information and belief are believed to be
true; and further that these statements were made with the
knowledge that willful false statements and the like so made
are punishable by fine or imprisonment, or both, under
Section 1001 of Title 18 of the United States Code and that
such willful false statements may jeopardize the validity of
the application or any patent issued thereon.

ALTERA CORPORATION
ASSIGNEE

11/20/03
Date

By: 
Derek E. Minihane
Director, Intellectual
Property Law,
and Assistant Secretary